

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BOARD OF PATENT APPEALS AND INTERFERENCES**

In re U.S. Patent Application

Applicant: Nate Mullen

Serial No.: 09/738,024

Group Art Unit No.: 3729

Filed: 12/14/2000

Examiner: Chang, Rick Kiltae

For: **METHOD OF WIRING LIGHTING FIXTURES TO ACHIEVE UNIFORM
VOLTAGE DROP**

**DECLARATION OF JOHN REEVES IN SUPPORT
OF APPELLANT'S APPEAL BRIEF**

1. I am a U.S. citizen residing in Temecula, California. I have personal knowledge of all facts stated in this declaration and if called as a witness, could and would testify competently to them under Oath.
2. I am an electrical engineer. I received a Higher National Certificate in Electrical Engineering with Endorsements in Electronics (equivalent to between a BSEE/MSEE) from Southwark Polytechnic in London, England in 1959. I have worked in the field of electrical engineering since 1956 for various engineering firms including Westinghouse Electric, Garatt Air Research, LH Research, California Instruments, Astec America, Mesa Power, and Hewlett Packard. As an electrical engineer I have prepared and reviewed hundreds of schematic drawings.
3. I have reviewed the Office Action dated January 30, 2004, Figure 5 of Hunte U.S. Patent No. 4,937,499, as cited in the Office Action, as well as, Nate Mullen's U.S. Patent Application No. 09/738,024.

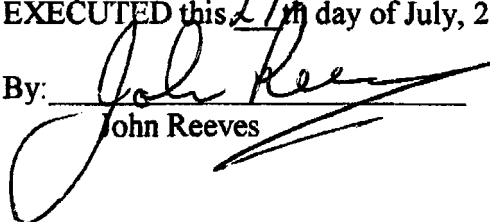
4. Based upon my investigation, experience, and analysis, it is my opinion that the light fixtures, elements 150a, 150b and 150c, and the connected lines and structures cited by the Examiner in the schematic diagram labeled Figure 5 of Hunte U.S. Patent No. 4,937,499, do not represent wire leads of uniform length.

5. In my experience and understanding, schematic drawings are not drawn to scale and the lines in a schematic drawing do not represent the precise proportions of the structures. The purpose of schematic drawings is to represent how connections are made in an electrical system or circuit. It is not a purpose of a schematic drawing to represent the length of wires or the size of electrical components in an electrical system or circuit.

I DECLARE under penalty of perjury under the laws of the United States that the foregoing is true and correct and based upon my personal knowledge.

EXECUTED this 27th day of July, 2004 at Temecula, California.

By:


John Reeves